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Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of _____:
Edward Barth, et al.

December 15, 2004
Group Art Unit: 2822

RECEIVED

DEC 15 2004

Serial No.: 10/659,778

Confirmation No. 5616
Examiner: Novacek, Christy L.

Filed: 9/10/03 :

International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

**TITLE: Dual-Damascene Interconnect Structure Using Low Stress Fluorosilicate Insulator with
Copper Conductors**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

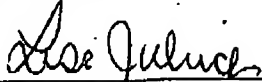
Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit the PTO-1449 and references attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Edward Barth, et al.

By 
Lisa J. Ulrich, Registration No. 45,168
Telephone No. 845-894-3338

FIS919990263US2

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10/059,118

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

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9/10/03

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,930,655	7/27/99	COONEY, III, ET AL.			
	5,451,543	9/19/95	WOO, ET AL.			
	5,753,564	5/19/98	FUKADA			
	5,930,655	7/27/99	COONEY, III, ET AL.			
	5,527,718	6/18/96	SEITA, ET AL.			
	5,661,334	8/26/97	AKRAM			
	6,214,730	4/10/01	COONEY, III, ET AL.			
	6,066,577	5/23/00	COONEY, III, ET AL.			
	6,310,300	10/30/01	COONEY, III, ET AL.			
	4,300,989	11/17/81	CHANG			
	5,011,732	4/30/91	TAKEUCHI, ET AL.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	JP07094606A2	04.07.95	JAPAN				
	KR94-12488	6/1994	KOREA				

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		S. M. Sze, VLSI Technology, second edition, p233-234, 266-267, 1988
		1990 Proceedings, Seventh International IEEE VLSI Multilevel Interconnection Conference, C.M. Dalton, p. 289-95, Jun. 1990.

EXAMINER

DATE CONSIDERED

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10/659,778

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	5,378,317	1/3/95	KASHIWASE, ET AL.			
	5,380,397	1/10/95	FUKAYAMA, ET AL.			
	6,004,883	12/21/99	YU, ET AL.			
	5,821,175	10/13/98	ENGELSBERG			
	6,040,243	3/21/00	LI, ET AL.			
	5,429,995	7/4/95	NISHIYAMA, ET AL.			
	6,333,257	12/25/01	AOI			
	3,620,833	11/1971	GLEIM, ET AL.			

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,219,791	6/15/93	FREIBERGER			
	5,306,671	4/26/94	OGAWA, ET AL.			
	5,380,397	1/10/95	FUKUYAMA, ET AL.			
	5,384,281	1/24/95	KENNEY, ET AL.			
	5,397,748	3/14/95	WATANABE, ET AL.			
	5,423,945	6/13/95	MARKS, ET AL.			
	5,451,543	9/19/95	WOO, ET AL.			
	5,466,638	11/14/95	EGUCHI			
	5,497,545	3/12/96	WATANABE, ET AL.			
	5,644,166	7/1/97	HONEYCUTT, ET AL.			
	5,181,985	1/26/93	LAMPERT, ET AL.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER	DATE CONSIDERED
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